

EVAL-IPFC01V1

3 kW three-channel interleaved PFC evaluation kit based on the STNRGPF01



Data brief

Features

- Input voltage range, Vin = 90 265 V AC
- Line frequency range, f = 47 63 Hz
- Maximum output power, Pout = 3 kW at 230 V
- Output voltage, Vout = 400 V
- Power factor (PF) > 0.98 at 20% load
- Total harmonic distortion, THD < 5% at 20% load
- Mixed-signal average current mode control, CCM fixed frequency operation
- Switching frequency, f_{sw} = 111 kHz
- Cycle-by-cycle regulation (analog current control loop)
- · Input voltage and load feedforward
- Phase shedding
- Burst-mode operation
- Overvoltage protection
- Thermal protection
- · Status indicator LEDs
- Inrush current limiter function
- Smart active cooling function

Description

The EVAL-IPFC01V1 is a 3 kW interleaved PFC board kit based on the STNRGPF01 three-channel interleaved CCM PFC digital controller. The kit is composed of the following three boards:

STEVAL-IPFC01P1 (power board)

STEVAL-IPFC01C1 (control board)

STEVAL-IPFC01A1 (adapter board)

These boards are sold only as part of the EVAL-IPFC01V1 kit, and cannot be ordered separately.

The STNRGPF01 is a digital configurable ASIC developed by STMicroelectronics, capable of driving up to three channels as interleaved PFC for industrial applications in order to meet IEC 61000-3-2 standards for electrical equipment.

The STNRGPF01 device implements mixed-signal (analog/digital) average current mode control in CCM at fixed frequency. The analog section ensures cycle-by-cycle current regulation while the digital section manages the loop and supervises the operations, giving the power stage adequate flexibility. The STNRGPF01 can be customized for specific applications using eDesign Suite, a dedicated software tool. For complete information on the STNRGPF01, please refer to the device datasheet.

Schematic diagrams EVAL-IPFC01V1

Schematic diagrams

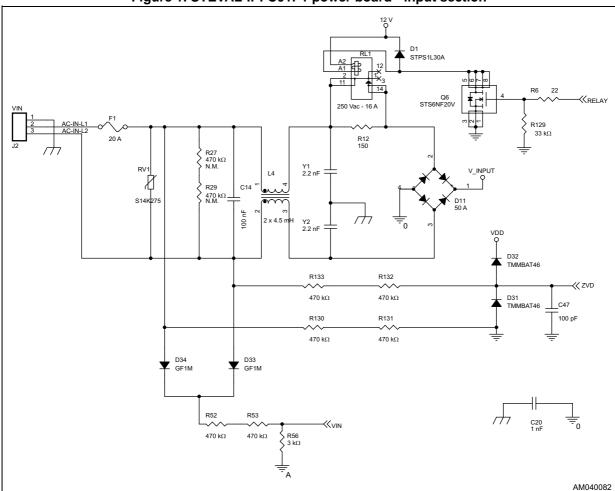


Figure 1. STEVAL-IPFC01P1 power board - input section

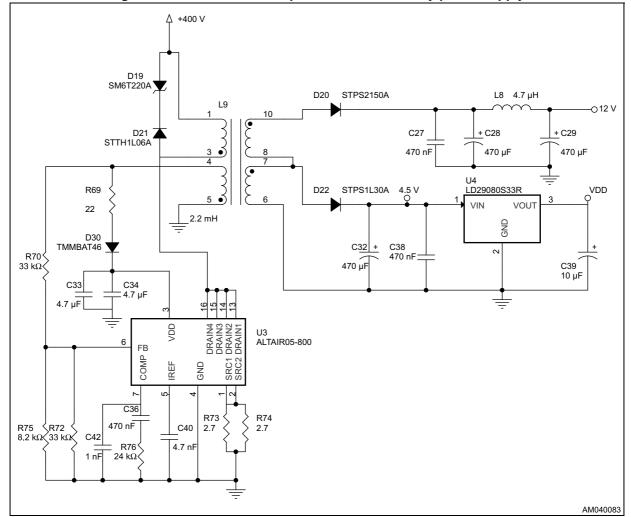


Figure 2. STEVAL-IPFC01P1 power board - auxiliary power supply

Schematic diagrams EVAL-IPFC01V1

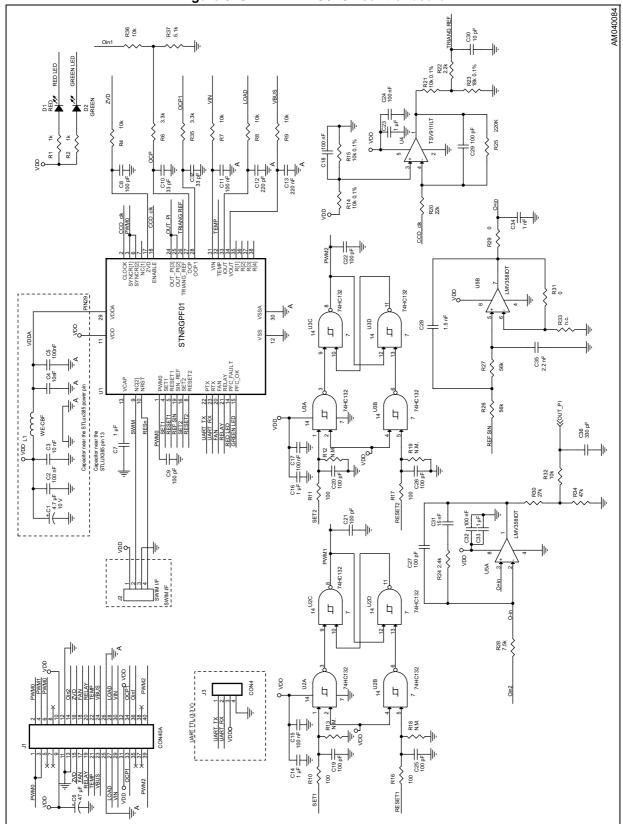


Figure 3. STEVAL-IPFC01C1 control board



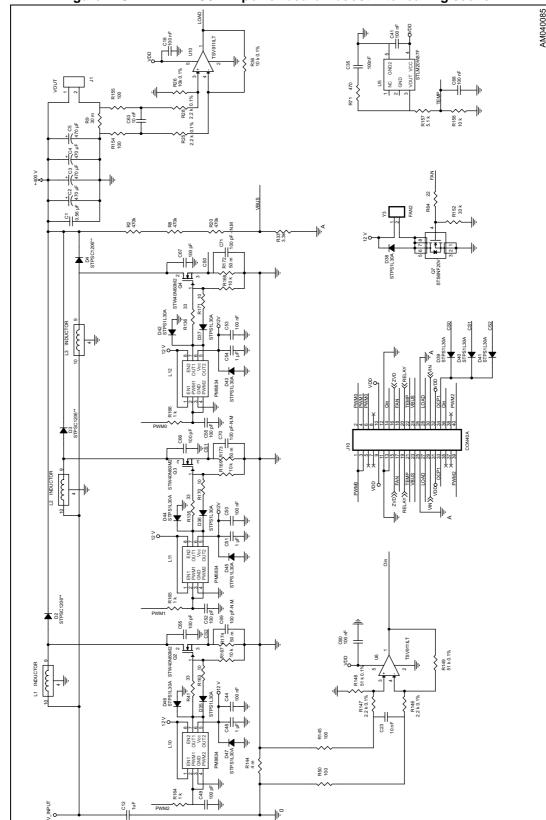


Figure 4. STEVAL-IPFC01P1 power board - boost interleaving section

Schematic diagrams EVAL-IPFC01V1

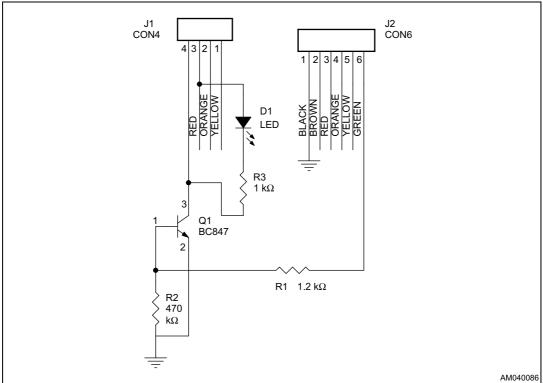


Figure 5. STEVAL-IPFC01A1 adapter board



EVAL-IPFC01V1 Revision history

Revision history

Table 1. Document revision history

Date	Revision	Changes
22-Mar-2017	1	Initial release.

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